

APPLICATION  
FOR  
UNITED STATES LETTERS PATENT

TITLE: MOBILE TELEPHONE SET

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09621045-072100

## MOBILE TELEPHONE SET

### BACKGROUND OF THE INVENTION

5 The present invention relates to a mobile telephone set using a liquid crystal display (LCD) for the display portion.

Fig. 3 is a block diagram showing the structure of an address data bus for a mobile telephone set using a LCD in its display portion. In Fig. 3, when information specifying an address is output from the CPU 1 for performing processing such as internal computations and controls, same information is transmitted to all of a data writable/readable internal RAM 2, an internal ROM 3 in which executable programs are stored, a DAC (D/A converter) 4 for converting signals from digital to analogue, a timer 5 for timing and counting an elapsed time, a RF controller 6 for controlling radio, a LCD controller 15 for controlling a liquid crystal display, a non-volatile SRAM 8, and a FLASH/ROM 9. In other words, it is constructed in such a manner that by controlling any one of blocks connected to the CPU 1 via a address data bus 10 shown above, the same information is transmitted to all other blocks connected to the CPU 1 via the address data bus 10.

However, in the mobile telephone set of recent years, the level of downsizing, weight reduction, and slimming down is ever increasing, and therefore, the structure is changing in such a manner that the LCD located on the upper part of the terminal is

getting closer to the antenna. As shown in Fig. 4, in the case where the structure is such that the LCD 13 mounted to the mobile telephone set 12 is located in the vicinity of the antenna 14, there is a recognized disadvantage in that the receiving

5 sensitivity of the antenna 14 is deteriorated due to the noise generated from the address data bus connected to the LCD 13. It is because the distance between the antenna 14 and the LCD 13 is getting shorter with the advance of downsizing, and consequently the noise from the address data bus connected to the LCD 13 is  
10 apt to be transmitted to the antenna 14.

It is conceivable to reduce the noise from the address data bus by lowering the voltage on the bus line frequently, however, since the CPU 1 is also connected to a block of which the access timing is fast, it is impossible to lower the voltage  
15 of the bus line frequently.

#### SUMMARY OF THE INVENTION

In order to overcome the problem as is described above, according to the first aspect of the invention, a mobile telephone set including display means for displaying information such as  
20 telephone numbers or characters comprises: a first address data bus connected to the principal Ics (integrated circuits) such as control means and storage means; and a second address data bus connecting the control means and the display means independently of the first address data bus; in that the control means controls  
25 the first address data bus and the second address data bus

independently.

According to the second aspect of the invention, in a mobile telephone set as mentioned above, communication means for transmitting signals via radio line is provided and the control means controls to prohibit access to the second address data bus while the communication means is receiving signals.

#### BRIEF DESCRIPTION OF THE DRAWINGS

Fig.1 is a block diagram showing the structure of the address data bus according to one embodiment of the invention;

Fig. 2 is a timing chart for the control of the LCD exclusive address data bus according to one embodiment of the invention;

Fig. 3 is a block diagram showing the structure of the address data bus of the related art; and

Fig. 4 is a drawing illustrating a positional relationship between the antenna and the LCD.

#### DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

Referring now to the drawings, the invention will be described according to an embodiment of the invention. The same reference numerals designate the identical parts throughout the drawings.

Fig. 1 is a block diagram showing the structure of the address data bus in a mobile telephone set according to an embodiment of the invention. Fig. 1 shows a construction comprising blocks of; a CPU 1 for performing processing such as

internal computations and controls, an data writable/readable  
internal RAM 2, an internal ROM 3 in which executable programs  
are stored, a DAC 4 for converting signals from digital to analogue  
(D/A converter), a timer 5 for timing and counting an elapsed time,  
5 a RF controller 6 for controlling radio, a LCD controller 7 for  
controlling the liquid crystal display, a non-volatile memory  
SRAM 8, and a FLASH\_ROM 9. The address data bus for transmitting  
information specifying address comprises a LCD exclusive address  
data bus 11 connected to the CPU 1 for the LCD controller 7 only  
10 which is independently provided from other blocks, and an address  
data bus 10 for other blocks which are commonly connected to the  
CPU 1.

In this arrangement, the address data bus 11 for the LCD  
controller 7 can be actuated independently, and thus the noise  
15 generated from the LCD exclusive address data bus 11 can be reduced  
by lowering the voltage of the LCD exclusive address data bus 11  
and degrading the driving ability.

Since the address data bus 11 for the LCD controller 7  
is independent of the address data bus 10 commonly used for other  
20 blocks, even when the CPU 1 performs fast access to the memory,  
the antenna is not affected thereby.

In this way, by using the LCD exclusive address data bus  
11 for the LCD controller 7 independently of other blocks, the  
LCD controller 7 can be held independently to minimize the noise  
25 from the address data bus 10 generated when blocks other than the

LCD controller 7 are accessed.

Fig. 2 is a timing chart showing an embodiment of the invention that controls the LCD exclusive address data bus in accordance with the transmission timing of radio signals.

5 It shows, from the top, transmission and receiving timings of radio signals, an access control of the address data bus commonly used by other blocks, and an access control timing of the LCD-exclusive address data bus. Since transmission of signals for controlling the LCD affects receiving of radio signals,  
10 access to the LCD is suspended during radio signals are being received.

According to the mobile telephone set of the invention having a construction as stated above, deterioration of receiving sensitivity of the antenna while the LCD is not accessed may be  
15 reduced. In addition, even when the LCD is being accessed, controlling the independent LCD exclusive address data bus enables to maintain rapid access to the memory, to reduce the noise of the LCD exclusive address data bus line, and to alleviate the degradation of receiving sensitivity of the antenna, thereby  
20 improving the performance of radio.

The structure as stated above enables to receive radio signals of high quality irrelevantly to further downsizing, weight reduction and slimming down of the mobile telephone set.